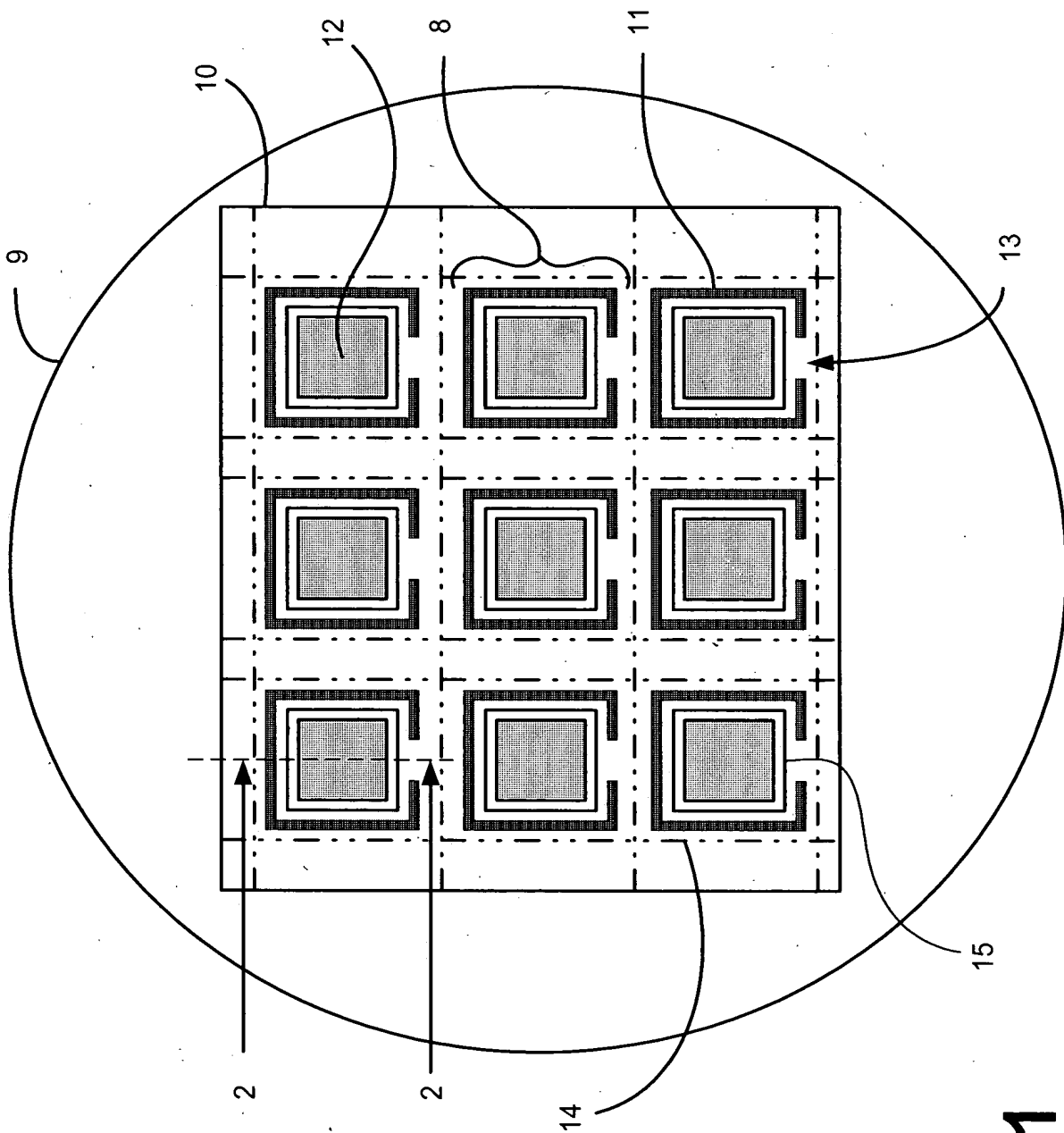
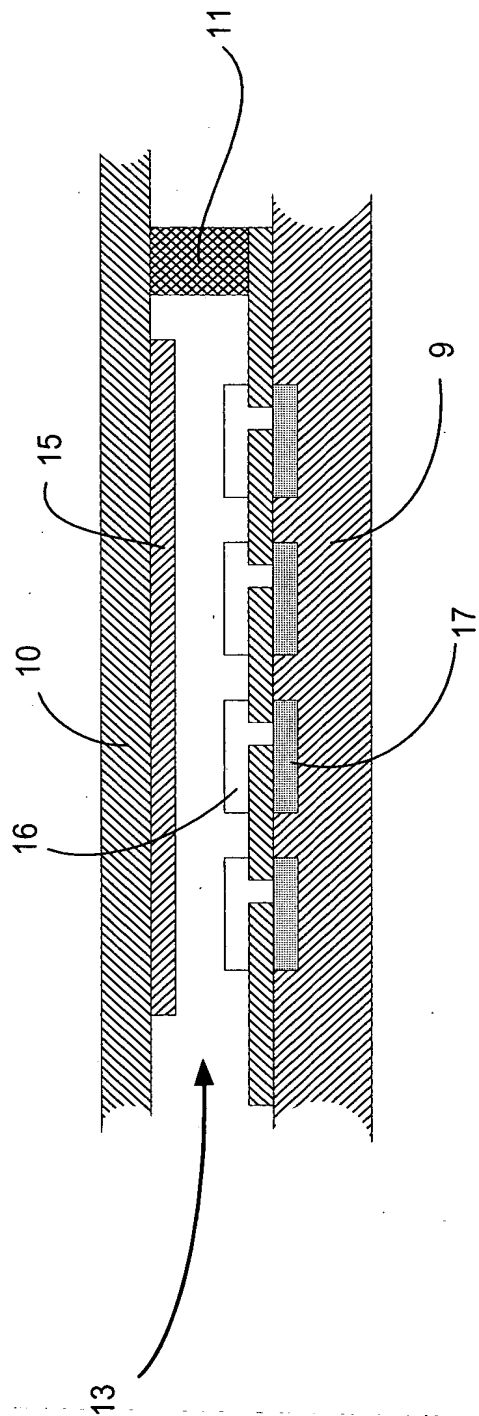


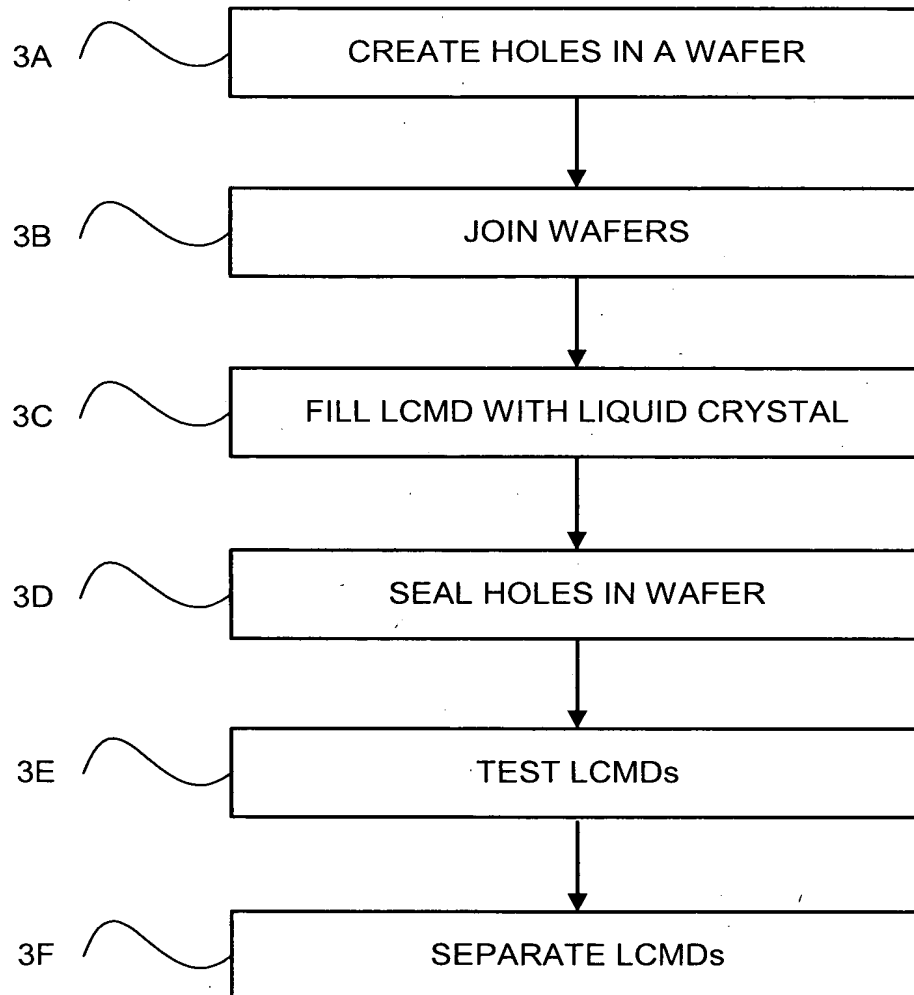
FIG. 1 is a schematic diagram of a prior art device. The device is a rectangular array of nine square elements arranged in a 3x3 grid. Each element consists of a central square region surrounded by a thick, dark border. The entire array is enclosed within a large circle. The diagram is labeled with various reference numerals: 2, 8, 9, 10, 11, 12, 13, 14, and 15.



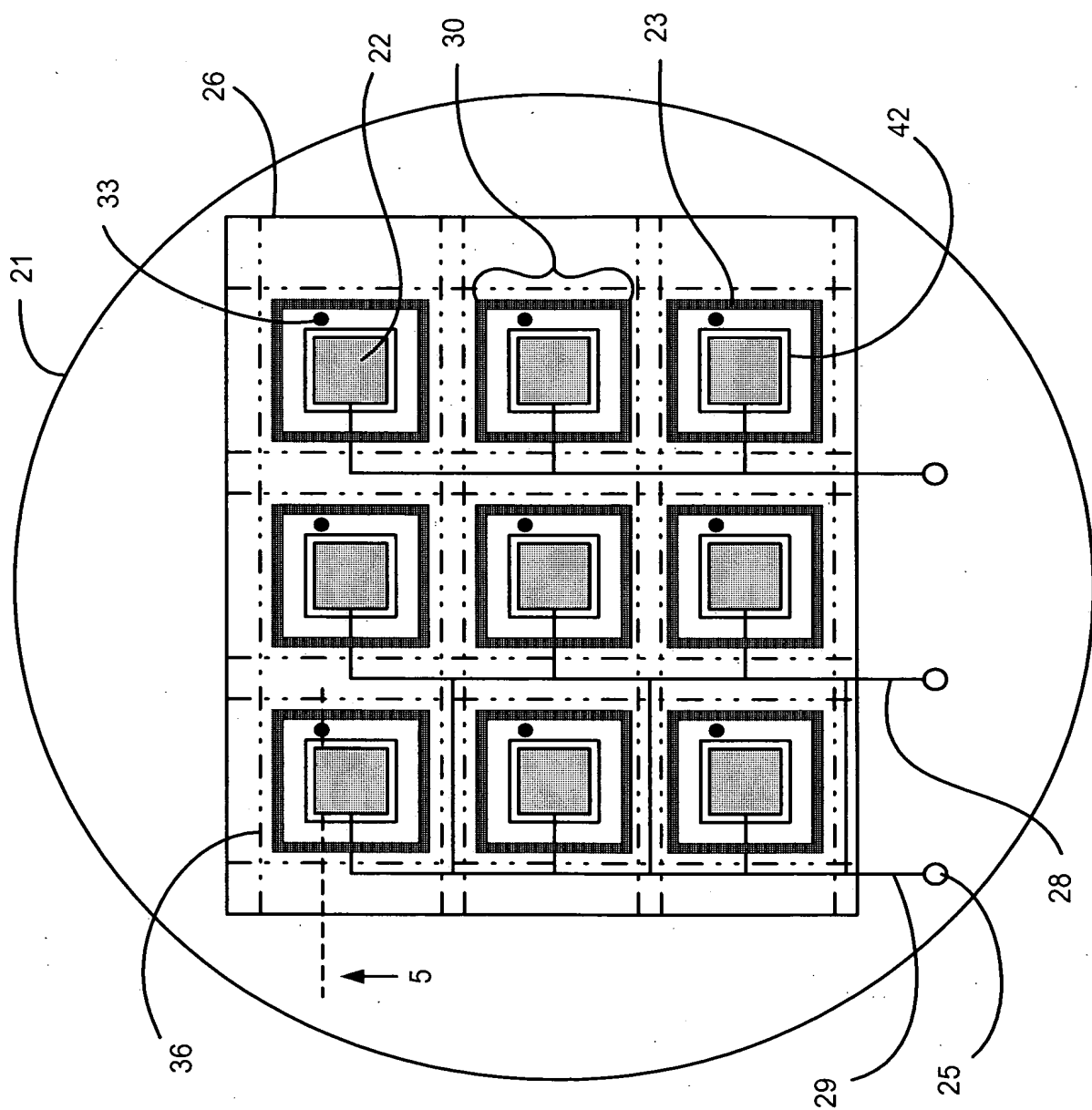
**FIG. 1**  
**PRIOR ART**



**FIG. 2**  
**PRIOR ART**



**FIG. 3**



**FIG. 4**

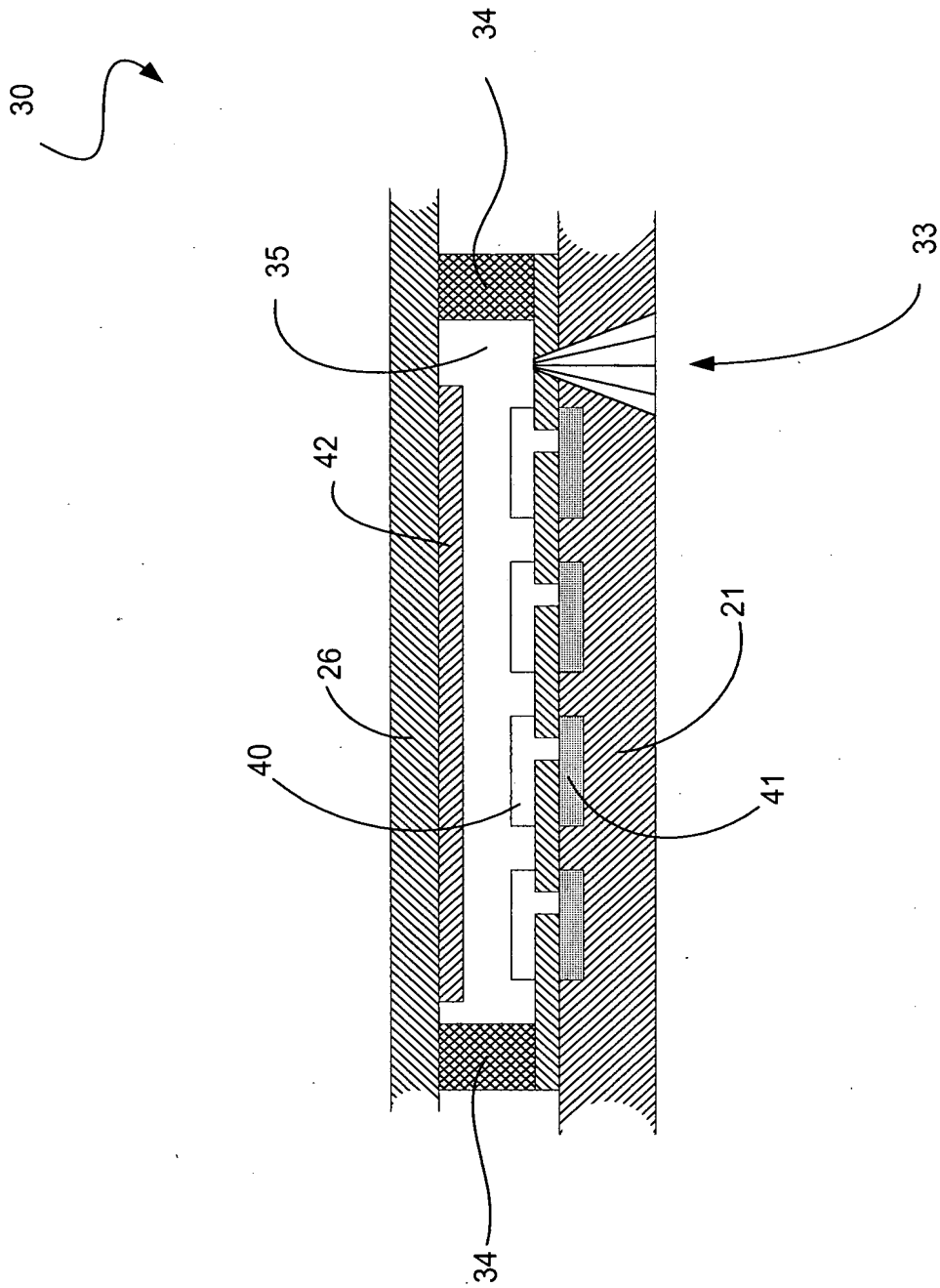


FIG. 5

FIG. 6 is a schematic diagram of a system for controlling a plurality of devices. The system includes a central control unit 40, a plurality of devices 41, and a power source 52. The central control unit 40 is connected to the devices 41 via a bus 53. The power source 52 is connected to the bus 53 via a switch 54. The switch 54 is controlled by a signal 56 from the central control unit 40.

FIG. 6

